

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant : Kazunori Yamashita  
Appl. No. : --  
Filed : Concurrently herewith  
For : A CIRCUIT BOARD CONNECTOR AND METHOD OF ASSEMBLING  
IT

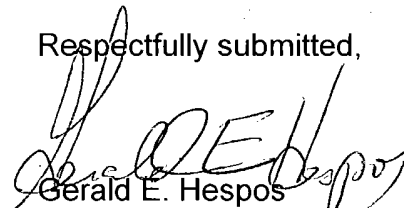
MS Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Sir:

This Information Disclosure Statement includes a copy of Form PTO-1449 that identifies one prior Japanese Unexamined Utility Model Publication. A copy of the identified reference is attached as well. The relevancy of this document is described in paragraphs 0002-0004 of the subject application. The Examiner is requested to consider this reference during the examination and to make the reference of record.

Respectfully submitted,



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Date: July 1, 2003

<b>INFORMATION DISCLOSURE CITATION</b> <i>(Use several sheets if necessary)</i>	Docket Number (Optional) <b>FP03-013US</b>	Application Number --
	Applicant(s) <b>Kazunori Yamashita</b>	
	Filing Date <b>Concurrently herewith</b>	Group Art Unit --

<b>U.S. PATENT DOCUMENTS</b>
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*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

<b>FOREIGN PATENT DOCUMENTS</b>
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	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
		<b>61-60486</b>	<b>1986</b>	<b>Japan</b>				<b>✓</b>

<b>OTHER DOCUMENTS</b> <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>
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<b>EXAMINER</b>	<b>DATE CONSIDERED</b>
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**EXAMINER:** Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.